

# VSA demonstration: The shifter problem

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# Motivation

- Vector Symbolic Architectures (VSAs) are a little known family of connectionist representation schemes (Plate; Kanerva; Rachkovskij; Gayler) that are able to address symbolic issues (e.g. compositional structures).
- Although VSAs are connectionist in form they are quite unlike “traditional” connectionist systems in behaviour.
- Researchers familiar with traditional connectionist systems may inappropriately generalise from them and fail to notice the unique capabilities of VSAs.
- The aim of this demonstration is to provide a simple example of the unique power of VSAs.
- This demonstration is novel, the VSA concepts are not.

# VSA architectural commitments

- Network of connectionist units
  - Single scalar output, multiple scalar inputs, fixed local numerical calculation
  - Small set of functional forms (VSA primitive operators)
- Sets of units are construed as vectors
  - High-dimensional vectors of unit outputs (1,000s to 10,000s of units)
- All vectors have the same dimensionality
  - Allows closed representation of composite structure (e.g. trees and graphs)
- Distributed representations on vectors
  - Representations appear random

# Abstract VSA primitives

- Mathematical primitives able to be implemented in connectionist hardware
  - The details are not critical (Plate 2003, pp 227-230)
- Operands are fixed dimension vectors
  - High dimensional (typically  $> 1,000$ )
  - Low resolution elements (1 bit is enough)
- Operators

	Mathematical	Hardware
Multiplication-like	$\times$	$\otimes$
Addition-like	$+$	$\oplus$
Permutation-like	$P_i()$	$\curvearrowright_i$

# Specific VSA primitives

	Real HRR (Plate)	Complex HRR (Plate)	Spatter code (Kanerva)	MAP code (Gayler)	Context- Dependent Thinning (Rachkovskij)
Vector elements	Real	Complex	Bits	Real (signs)	Bits
$\times$	Circular convolution	Element- wise product	Element- wise XOR	Element- wise product	Element-wise OR and AND
$+$	Element- wise sum	Element- wise sum	Element- wise majority	Element- wise sum	

# Levels of description

	<b>VSA</b>	<b>Computer</b>
<b>Vector Representation</b>	<b>High-dimensional vectors &amp; Vector operators (<math>\otimes</math>, <math>\oplus</math>, <math>P(\cdot)</math>)</b>	<b>Binary words &amp; Logical operators (AND, XOR, etc)</b>
<b>Representational Architecture</b>	<b>Fixed network of operators</b>	<b>CPU</b>
<b>Cognitive Architecture</b>	<b>Mapping of task onto lower levels</b> (representational idioms, population of memory, etc.)	<b>Program</b> (data structures, function definitions, etc.)

The importance of VSA lies in what is made easy

# Similarity

- Connectionist approaches depend on vector similarity
  - Similar input vectors (generally) lead to similar outputs
- Define similarity as the normalised dot product
  - Correlation of the vectors
  - Cosine of angle between the vectors
- $E[\text{corr}(\mathbf{a}, \mathbf{b})] = 0$  (distribution over random  $\mathbf{a}$  and  $\mathbf{b}$ )
- $\text{sd}[\text{corr}(\mathbf{a}, \mathbf{b})] = \textit{small}$  (scales as  $n^{-0.5}$ )
- Random pairs of vectors generally maximally dissimilar
  - Symbol-like behaviour (vectors generally same or different)
  - Graded similarity can be constructed as a special case
  - $E[\text{corr}(\mathbf{a}, \mathbf{a} + \mathbf{b})] \gg \text{sd}[\text{corr}(\mathbf{a}, \mathbf{a} + \mathbf{b})]$

# Distribution of similarity

Plate, T.A. (1994) *Distributed representations and nested compositional structure*. Ph.D. thesis, Department of Computer Science, University of Toronto

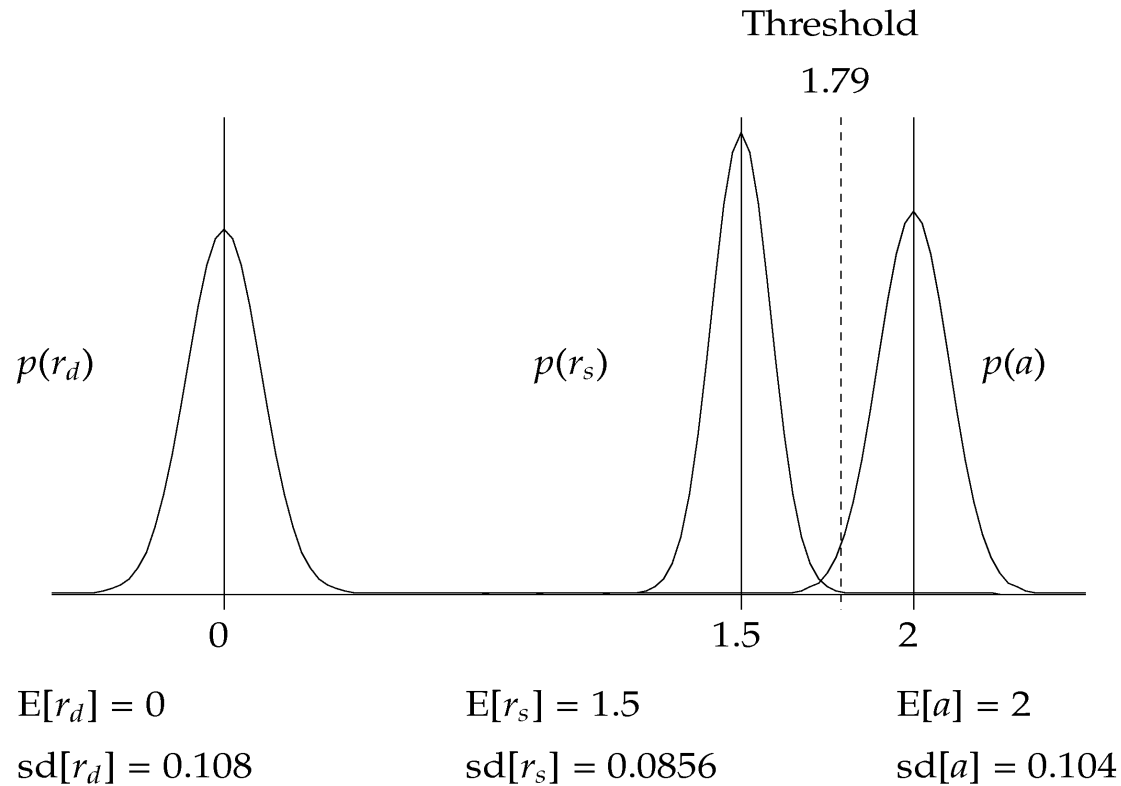


Figure B.4: Probability density functions for signals in a superposition memory with similarity among vectors, with  $n = 512$ ,  $m = 1000$ ,  $|\mathbb{E}_p| = 100$ ,  $\alpha^2 = 0.5$ , and  $k = 3$ .



# Contribution of sign & magnitude

Plate, T.A. (1994) *Distributed representations and nested compositional structure*. Ph.D. thesis, Department of Computer Science, University of Toronto

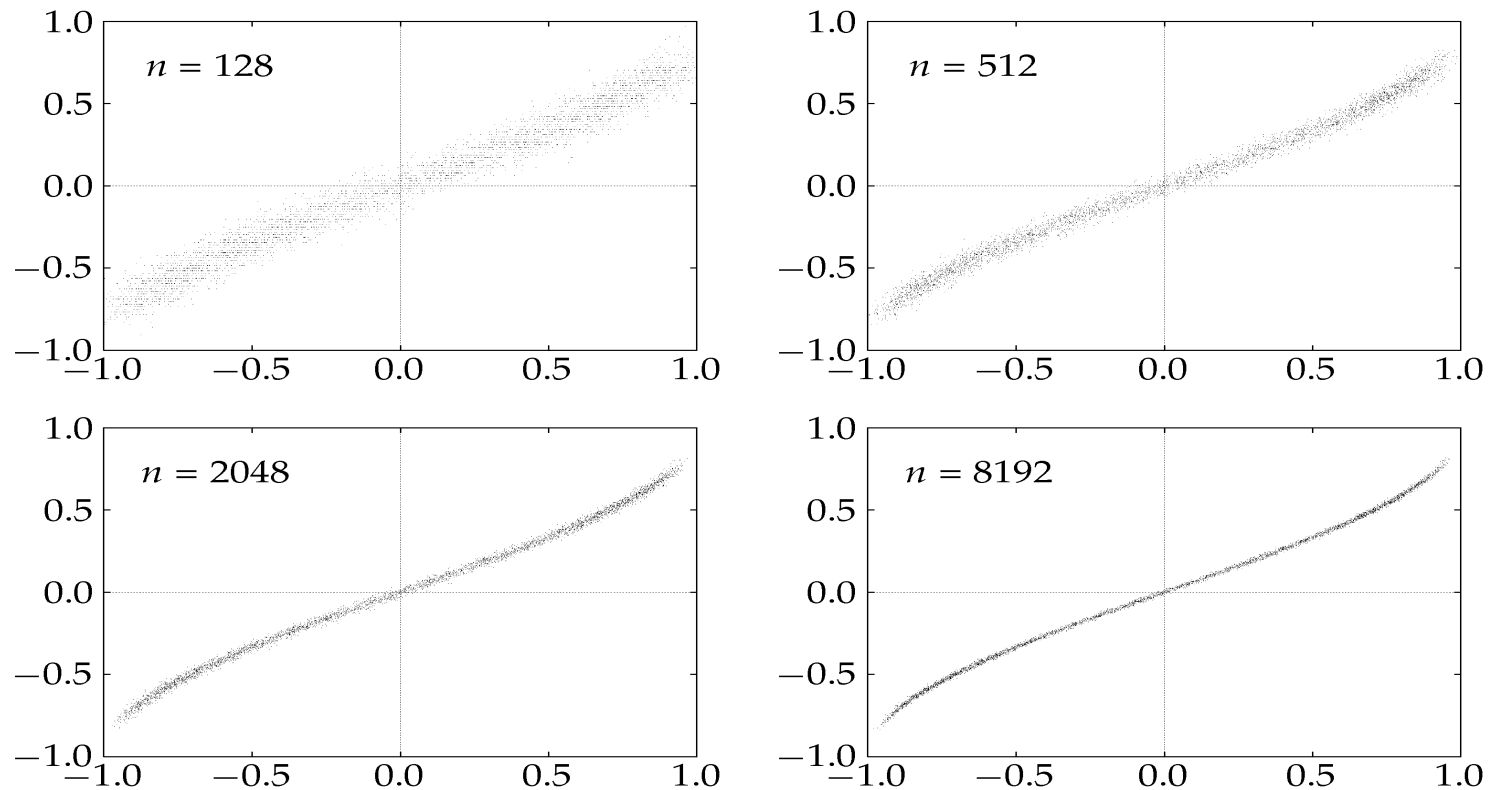


Figure 3.8: Bitwise dot-products (vertical axis) versus floating-point dot-products (horizontal axis) for various vector dimensions.

# Bundling (+)

- Used to create weighted sets (intuitive interpretation)
- The bundle is the superposition of its constituents
- The bundle operator preserves similarity
  - The resulting vector is **similar** to all its constituent vectors.
- Example: given 10,000-dimensional random vectors  $\mathbf{x}$ ,  $\mathbf{y}$ ,  $\mathbf{z}$ , and  $\mathbf{r}$ , we can calculate  $\mathbf{m} = \mathbf{x} + \mathbf{y} + \mathbf{z}$ 
  - The correlation coefficients of the resulting vector with its constituents are around 0.5:  
 $\text{corr}(\mathbf{x}, \mathbf{m}) = 0.50$ ,  $\text{corr}(\mathbf{y}, \mathbf{m}) = 0.48$ ,  $\text{corr}(\mathbf{z}, \mathbf{m}) = 0.51$
  - While for another random vector  $\mathbf{r}$  not a constituent of  $\mathbf{m}$ :  
 $\text{corr}(\mathbf{r}, \mathbf{m}) = 0.01$
- Bundles can be probed and can be used holistically

# Binding ( $\times$ )

- Used to create structural associations (intuitive)
- The binding is the association of its components
- The components may be retrieved from the binding  
Generically, given  $\text{bind}(\mathbf{a}, \mathbf{b})$ :  
 $\text{unbind}(\text{bind}(\mathbf{a}, \mathbf{b}), \mathbf{a}) \approx \mathbf{b}$   
 $\text{unbind}(\text{bind}(\mathbf{a}, \mathbf{b}), \mathbf{b}) \approx \mathbf{a}$   
 $\text{unbind}(\text{bind}(\mathbf{a}, \mathbf{b}), \mathbf{c}) \approx \mathbf{0}$
- Details of  $\text{bind}(\cdot, \cdot)$  and  $\text{unbind}(\cdot, \cdot)$  vary between VSAs
- The bind operator partially destroys similarity  
 $\text{corr}(\text{bind}(\mathbf{a}, \mathbf{b}), \mathbf{a}) \approx 0$   
 $\text{corr}(\text{bind}(\mathbf{a}, \mathbf{b}), \text{bind}(\mathbf{a}', \mathbf{b})) = \text{corr}(\mathbf{a}, \mathbf{a}')$
- Bindings may be bundled and used holistically

# Implementations of binding

- Details of  $\text{bind}(\cdot, \cdot)$  and  $\text{unbind}(\cdot, \cdot)$  vary between VSAs
  - HRR:  $\text{bind} \equiv$  circular convolution,  $\text{unbind} \equiv$  circular correlation
  - BSC & MAP:  $\text{bind} \equiv \text{unbind} \equiv$  element-wise product (XOR,  $\times$ )
- In BSC & MAP every vector is self-inverse w.r.t.  $\text{bind}(\cdot, \cdot)$ :  
 $(+1) \times (+1) = +1$ ;  $(-1) \times (-1) = +1$ ;  $\mathbf{a} \times \mathbf{a} = \mathbf{1}$
- Whether binding or unbinding occurs depends on the relation between the operands:  
 $\mathbf{a} \times \mathbf{b} = (\mathbf{a} \times \mathbf{b})$  (binding)  
 $\mathbf{a} \times (\mathbf{a} \times \mathbf{b}) = (\mathbf{a} \times \mathbf{a}) \times \mathbf{b} = \mathbf{1} \times \mathbf{b} = \mathbf{b}$  (unbinding)
- This makes design more difficult
- But allows substitution as a primitive operation
  - Substitution traditionally seen as a symbolic operation

# Substitution

- The association of two vectors can be used as a substitution operator:
  - $(\mathbf{a} \times \mathbf{b})$  (substitutes  $\mathbf{a}$  for  $\mathbf{b}$  and vice versa)
  - $(\mathbf{a} \times \mathbf{b}) \times (\mathbf{a} \times \mathbf{x}) = \mathbf{a} \times \mathbf{a} \times \mathbf{b} \times \mathbf{x} = (\mathbf{a} \times \mathbf{a}) \times \mathbf{b} \times \mathbf{x} = (\mathbf{b} \times \mathbf{x})$
  - $(\mathbf{a} \times \mathbf{b}) \times (\mathbf{b} \times \mathbf{x}) = \mathbf{b} \times \mathbf{b} \times \mathbf{a} \times \mathbf{x} = (\mathbf{b} \times \mathbf{b}) \times \mathbf{a} \times \mathbf{x} = (\mathbf{a} \times \mathbf{x})$
- $\mathbf{a}, \mathbf{b}, \mathbf{x}$  can represent complex structures
  - The hardware sees them only as vectors (blind to complexity)
- Substitution can be applied holistically to a bundle
  - $(\mathbf{a} \times \mathbf{b}) \times (\mathbf{a} \times \mathbf{x} + \mathbf{a} \times \mathbf{y} + \mathbf{a} \times \mathbf{z}) = (\mathbf{b} \times \mathbf{x} + \mathbf{b} \times \mathbf{y} + \mathbf{b} \times \mathbf{z})$
- Substitution is necessarily systematic

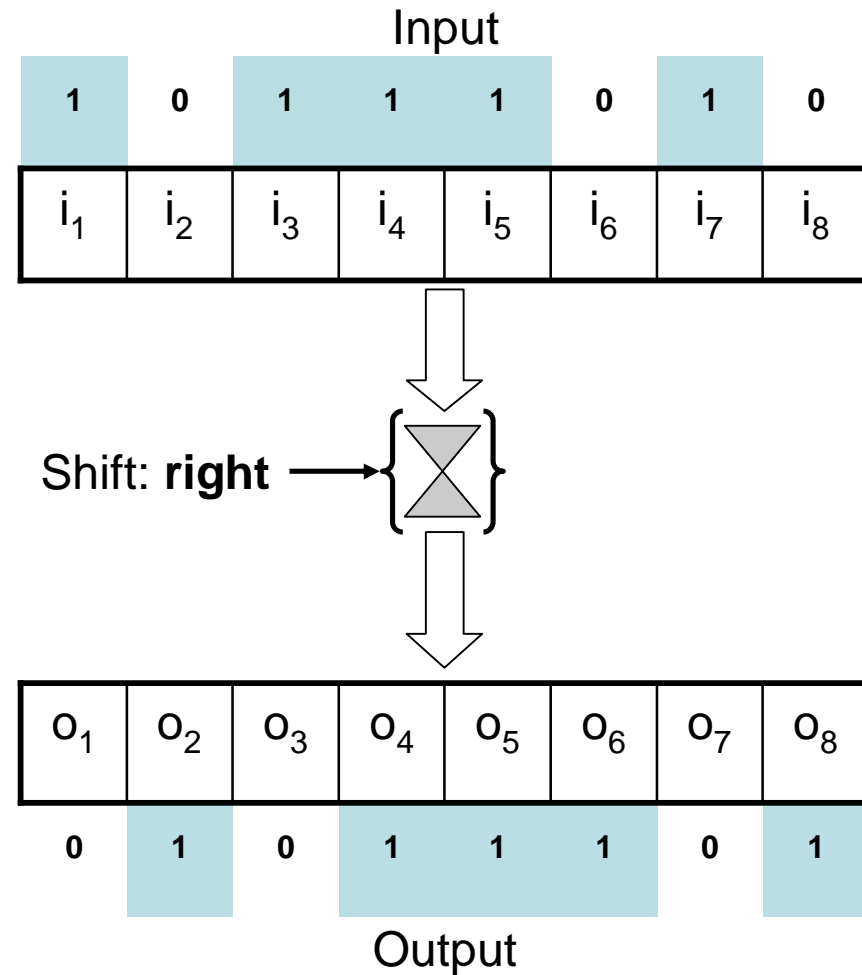
# Cleanup memory

- Results of operations on bundles usually approximate
  - $(\mathbf{a}) \times (\mathbf{a} \times \mathbf{b} + \mathbf{c} \times \mathbf{d} + \mathbf{e} \times \mathbf{f} + \dots)$
  - $= \mathbf{b} + \mathbf{a} \times \mathbf{c} \times \mathbf{d} + \mathbf{a} \times \mathbf{e} \times \mathbf{f} + \dots$
  - $= (\mathbf{b} + \boldsymbol{\varepsilon})$
  - Where  $\text{corr}((\mathbf{b} + \boldsymbol{\varepsilon}), \mathbf{b}) = \textit{large}$
- We can tell the result contains  $\mathbf{b}$  but need an extra step if we want to extract  $\mathbf{b}$  as a signal
- There are no inherently distinguished vectors (directions)
- Cleanup memory distinguishes vectors we choose  
 $\text{cleanup}((\mathbf{b} + \boldsymbol{\varepsilon}), \textit{item-set}) = \mathbf{b}$

e.g.  $\text{result} = (\text{input} \odot \textit{item}) \textit{item}$

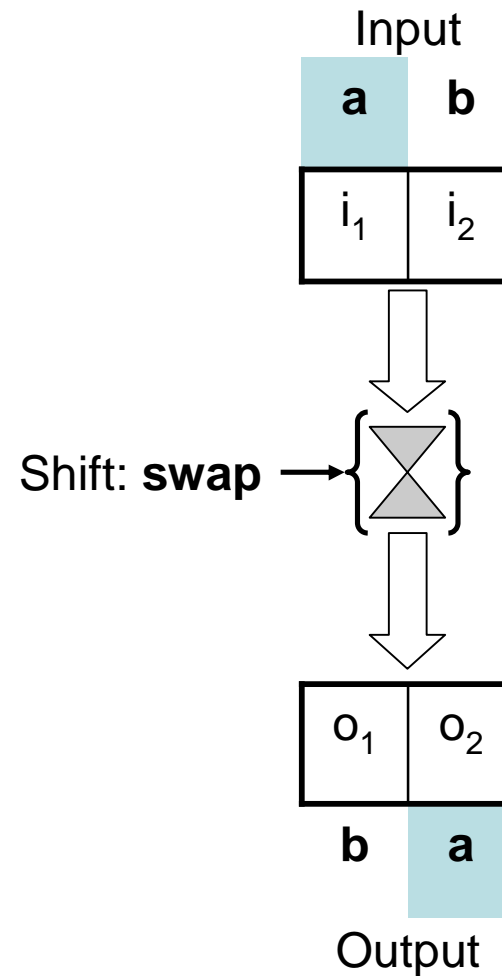
# The shifter problem

- PDP (1986) V1 pp299-303
- Registers
  - 8 bits in
  - 8 bits out
  - shift (left, right, none)
- Boltzmann net
- Train: Show 360,000 <in,shift,out> triples
- Test: Recognise the shift from the input & output
  - 50% to 89% correct (fn. of number of active bits)



# The demo problem (modified)

- Registers
  - 2 arbitrary patterns in
  - 2 arbitrary patterns out
  - shift (swap, none)
- Patterns (not bits)
  - More general than bits
  - Shifts are unambiguous if unique patterns used
- Shorter registers
  - Fewer possible shifts
  - Less information load
  - More ambiguous if patterns are not unique



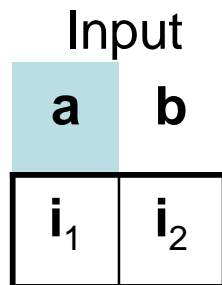


# Encoding the shifter problem

- Represent the input and output registers as “frames”
  - A frame is a set of  $\langle \text{role}, \text{value} \rangle$  pairs
- Each position in a register is a “role”
- Bind the input and output register frames with the shift
  - $\langle \text{input}, \text{shift}, \text{output} \rangle$  triples
- This choice of representation is not unique, but it is simple, natural, and works
- Store  $\langle i, s, o \rangle$  triples as examples in the memory trace
- Query the memory trace with novel  $\langle i, s, o \rangle$  fragments to test retrieval and generalisation

# Encoding the input register

## Input Register



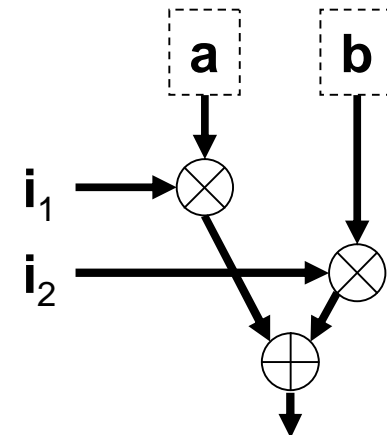
- **a** and **b** are input patterns
- **i<sub>1</sub>** and **i<sub>2</sub>** identify the positions of the input register

## Mathematical

$$(\mathbf{i}_1 \times \mathbf{a} + \mathbf{i}_2 \times \mathbf{b})$$

- **a** and **b** are inputs
- **i<sub>1</sub>** & **i<sub>2</sub>** constants
- values are vectors
- named vectors are approx. orthogonal

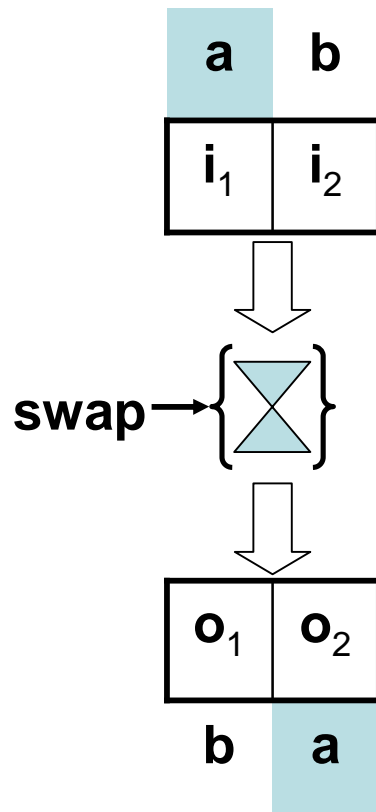
## Hardware



- arrows carry vector values
- **a** and **b** are vector inputs
- **i<sub>1</sub>** and **i<sub>2</sub>** are vector constants

# Encoding a complete example

## Shift Register

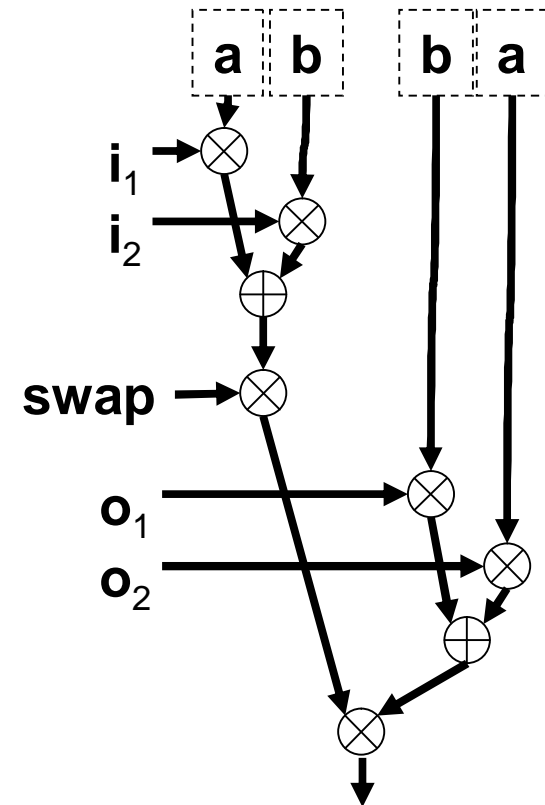


## Mathematical

$$(i_1 \times a + i_2 \times b) \\ \times \text{swap} \times \\ (o_1 \times b + o_2 \times a)$$

- **swap** and **none** are vector constants

## Hardware



# Every vector is just a vector

- The encoding of an example looks complex
$$\begin{aligned} & (\mathbf{i}_1 \times \mathbf{a} + \mathbf{i}_2 \times \mathbf{b}) \times \text{swap} \times (\mathbf{o}_1 \times \mathbf{b} + \mathbf{o}_2 \times \mathbf{a}) \\ &= (\mathbf{i}_1 \times \mathbf{a} \times \text{swap} + \mathbf{i}_2 \times \mathbf{b} \times \text{swap}) \times (\mathbf{o}_1 \times \mathbf{b} + \mathbf{o}_2 \times \mathbf{a}) \\ &= \mathbf{i}_1 \times \mathbf{a} \times \text{swap} \times \mathbf{o}_1 \times \mathbf{b} + \mathbf{i}_2 \times \mathbf{b} \times \text{swap} \times \mathbf{o}_1 \times \mathbf{b} + \mathbf{i}_1 \times \mathbf{a} \times \text{swap} \times \mathbf{o}_2 \times \mathbf{a} + \\ & \quad \mathbf{i}_2 \times \mathbf{b} \times \text{swap} \times \mathbf{o}_2 \times \mathbf{a} \\ &= \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_1 \times \mathbf{a} \times \mathbf{b} + \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_1 \times (\mathbf{b} \times \mathbf{b}) + \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_2 \times (\mathbf{a} \times \mathbf{a}) + \\ & \quad \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_2 \times \mathbf{b} \times \mathbf{a} \\ &= \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_1 \times \mathbf{a} \times \mathbf{b} + \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_1 \times \mathbf{1} + \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_2 \times \mathbf{1} + \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_2 \times \mathbf{b} \times \mathbf{a} \\ &= \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_1 \times \mathbf{a} \times \mathbf{b} + \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_1 + \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_2 + \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_2 \times \mathbf{b} \times \mathbf{a} \\ &= \text{swap} \times \mathbf{i}_2 \times \mathbf{o}_1 + \text{swap} \times \mathbf{i}_1 \times \mathbf{o}_2 + (\text{input-output value dependent stuff}) \end{aligned}$$
- But as far as the hardware is concerned it is just a vector like any other. The decomposition into terms is in the mind of the analyst and not visible to the hardware.

# Memory traces in MAP coding

- A memory trace is a vector ( $\mathbf{T}_t$ ) from which values can be retrieved.
- The memory trace is updated as items are added
- Add an association to the memory trace

$$\mathbf{T}_{t+1} = \mathbf{T}_t + \mathbf{a} \times \mathbf{b}$$

- Retrieve from the memory trace using  $\mathbf{a}$  as the cue

$$\text{retrieve}(\mathbf{T}_{t+1}, \mathbf{a}) = \mathbf{T}_{t+1} \times \mathbf{a}$$

$$= (\mathbf{a} \times \mathbf{b} + \mathbf{T}_t) \times \mathbf{a}$$

$$= \mathbf{a} \times \mathbf{a} \times \mathbf{b} + \mathbf{T}_t \times \mathbf{a}$$

$$= \mathbf{a} \times \mathbf{a} \times \mathbf{b} + \sim \mathbf{0}$$

$$\approx \mathbf{a} \times \mathbf{a} \times \mathbf{b}$$

$$\approx (\mathbf{a} \times \mathbf{a}) \times \mathbf{b}$$

$$\approx \mathbf{1} \times \mathbf{b}$$

$$\approx \mathbf{b}$$

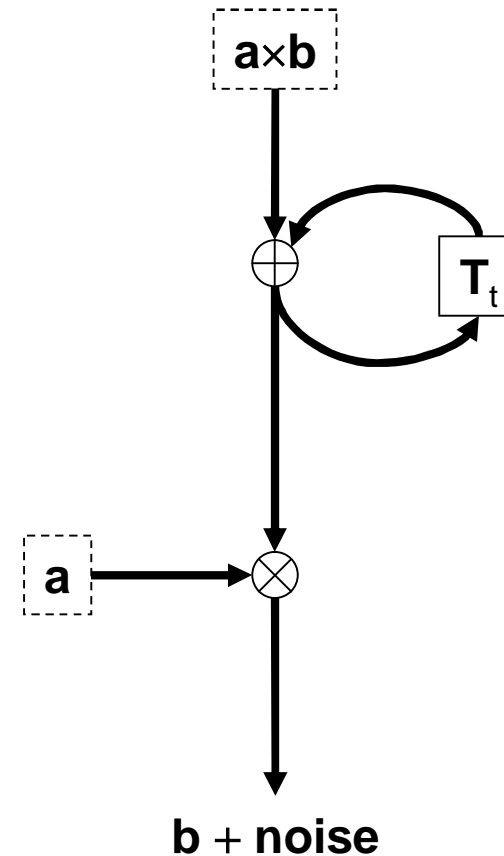
# Adding To and Querying the Memory Trace

Mathematical

$$\mathbf{T}_{t+1} = \mathbf{T}_t + \mathbf{a} \times \mathbf{b}$$

$$\begin{aligned} \text{retrieve}(\mathbf{T}_{t+1}, \mathbf{a}) \\ = \mathbf{T}_{t+1} \times \mathbf{a} \end{aligned}$$

Hardware



# Value independent substitution

- The memory trace of a single example contained terms like: **swap** $\times$ **i**<sub>1</sub> $\times$ **o**<sub>2</sub>
- These terms can be applied as substitution operators
- They are independent of the actual input/output values
- When we query the memory of examples with a partial cue (e.g. (**i**<sub>1</sub> $\times$ **p** + **i**<sub>2</sub> $\times$ **q**) $\times$  **swap**) the role vectors are substituted to yield the expected answer
$$(\mathbf{swap} \times \mathbf{i}_2 \times \mathbf{o}_1 + \mathbf{swap} \times \mathbf{i}_1 \times \mathbf{o}_2 + \dots) \times ((\mathbf{i}_1 \times \mathbf{p} + \mathbf{i}_2 \times \mathbf{q}) \times \mathbf{swap})$$
$$\approx (\mathbf{o}_1 \times \mathbf{q} + \mathbf{o}_2 \times \mathbf{p})$$
- Generalises to novel values (if in cleanup memory)
- Can generalise from one example